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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10072316	02/08/2002	257		2811	Loke

**APPLICANTS: Karasawa Junichi; Watanabe Kunio;

**CONTINUING DATA VERIFIED: none
Loke

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-088309 03/26/2001

JAPAN 2001-330785 10/29/2001 Loke

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		81751.0029
Verified and Acknowledged Examiners's Initials Loke		
TITLE : Semiconductor device, memory system and electronic apparatus		

U.S. DEPT. OF COMM /PAT. & TM-PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Amount Due		Print Fig.	
Date Paid			
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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